S/N 10/810,035 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

 Applicant:
 Albert S. Weiner
 Examiner:
 Thinh Nguyen

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Title: Non-volatile transistor memory array incorporating read-only elements with single

mask set

## INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached PTO 1449 Form be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the PTO 1449 Form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. § 1.98(a)(2), copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R § 1.98(a)(2).

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

The Examiner is invited to contact the Applicant's Representative at the telephone number indicated if there are any questions regarding this communication.

Respectfully submitted,

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Date 19 August 2009 I

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Wgb, and is addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this in 19/2 and of August, 2009.

Name Mosic Jaco

Signature